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10/13/2005 CCHAU2 00000051 10637848

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(Depositor's name)	Reagan Days
(Signature)	
(Date)	October 5, 2005

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/637,848	08/07/2003	Lap-Wai Chow	B-4505NP 620843-6	3340

TITLE OF INVENTION: USE OF SILICON BLOCK PROCESS STEP TO CAMOUFLAGE A FALSE TRANSISTOR

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APPLN. TYPE	SMALL ENTITY	ISSUE F	EE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
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Typed or printed name	Robert Popa	9		Registration	_{1 №.} 43,010	

This collection of information is required by 37 CFR 1.311. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 12 minutes to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, Virginia 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

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